Pulse Width Modulated AC Voltage Controller Filter Design by Optimization Technique

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Abstract The AC voltage controller plays a vital role in many of the industries. In this paper the multiple pulse width modulated AC voltage controllers is designed for analyzing the different loads. The performance of AC voltage controller is analyzed with and without filter. The performance parameters are output voltage and total harmonic distortion. The performance analysis is done for pulse width modulated AC voltage controller with resistive load and combination of resistive and inductive load. The design of passive filter components is obtained using particle swarm bacterial foraging optimization technique. The effect of filter requirements is analyzed using the Matlab software.

Keywords— Filter component, optimization technique, total harmonic distortion, AC Voltage controller

I. INTRODUCTION

The AC Voltage controller plays important role in industries for the application on controlling high speed fan and pump control systems[8]. The cost and economy of the controller is cheap compared to other different methods of speed control. In the literature different topologies is described like TRIAC control, phase angle control and single pulse width modulation techniques [15]. TRIAC control is the simplest and economic voltage regulators but the harmonic in the output voltage is more. The phase angle control is best suited for different load conditions for energy saving mechanisms but the distortion in the waveform is more at larger triggering angle. Single pulse width modulation is utilized for ac voltage controllers.

From the literature it is understood that phase control is better compared to single pulse width modulation for energy saving strategy [11]. The multiple pulse width modulation and extinction angle control shows better performance compared to other conventional controllers [9]. The passive filter and active filters plays vital role in making the waveform smooth and maintain the total harmonic distortion within the limits. The design of passive filter is utmost importance for enhancing the quality of ac voltage controllers.

The novel method of harmonic elimination method compared to fixed duty cycle method for removing lower order harmonics [1]. This result gives good indication of using new technique for eliminating lower order harmonics. This requires further investigation of eliminating other harmonics. This paper gives insight view of using optimal capacitor for AC chopper was done through simulations [2]. This can be done by hardware for further enhancement in studies. The new concept of series resonant conversion for AC chopper was studied. Finally a simple passive filter is used to filter the harmonics [3]. The total harmonic distortion is less that 5% from hardware realization. This paper analyses conventional method of ac voltage controller and this paper uses symmetrically controlled technique to reduce the harmonics. The main drawback is use of force commutation which causes major effect on output voltage [4]. This paper focuses on design of input and output filters of ac voltage controllers. The major drawback of this method is duty cycle affects the power factor and hence optimal capacitor design is required [5].

The main objective of this paper is to analyse the steady state equivalent circuit pulse width modulation of ac voltage controller for resistive and resistive-inductive load. Initially the performance is analysed without passive filter components for multiple pulse width modulation of ac voltage controller for both resistive and resistive-inductive load. The transfer function for ac voltage controller with resistive and resistive-inductive load is derived. From this the power factor is calculated and taken as objective function for the optimization algorithm. The optimization is performed for suitable values of filter inductance and capacitance to minimize the objective function. The performance parameters for the ac chopper are total harmonic distortion and pure sinusoidal of ac output voltage.

II. ANALYSIS OF PWM AC VOLTAGE CONTROLLER

The pulse width modulation of AC voltage controller plays vital role in industries [6]. The power circuit consists of main IGBT with four diodes and for freewheeling action has supplementary IGBT with four diodes are formulated as power circuit and the diagram is shown in figure1. During positive half cycle the main IGBT will conduct through diodes D1 and D2 and pass through load and return back to supply. The freewheeling action takes place when main device is off and supplementary device is on. During negative half cycle current passes through load, diodes D3 , main device and diode D4 and
return back to supply. From this operation it is implied that pulse generation is designed such that main and supplementary IGBT will not conduct simultaneously.

![PWM AC voltage controller with R Load](image1)

**Fig 1. PWM AC voltage controller with R Load**

![PWM AC voltage controller with RL Load](image2)

**Fig 2. PWM AC voltage controller with RL Load**

Under steady state conditions the output voltage of AC voltage controller is given by [2]

\[ V_o = D \cdot V_i \]  

(1)

The steady state equivalent circuit is given by

![Steady State Equivalent circuit](image3)

**Fig 3. Steady State Equivalent circuit**

The filter inductance design can be obtained by the formula

\[ L = \frac{V_m \cdot T(1 - D)}{I_{ripple}} \]  

(2)

The capacitance value should be greater than the designed value.

\[ C = \frac{V_m \cdot T^2 (1 - D)}{8 \cdot L \cdot V_{ripple}} \]  

(3)

The main purpose of passive filter in AC voltage controller gives distortion free output. The filter inductor should be optimum value such that the harmonics in the output voltage is less and the larger value of inductance causes power dissipation in the filter circuit is more. The value of capacitance decreases the angle between the voltage and current gives near unity power factor. In the equivalent circuit the impedance is taken as resistive load \( R_a \). \( D \) is the duty ratio, \( L \) and \( C \) are filter inductance and capacitance respectively. The transfer function for the above equivalent circuit with resistive load is a second order function.

\[ V_o(s) = \frac{D \cdot V_i(s)}{s^2 \cdot L \cdot C + s \cdot \frac{L}{R_a} + 1} \]  

(4)

The final transfer function is given as

\[ \frac{V_o(s)}{V_i(s)} = \frac{D \cdot R_a}{s^2 \cdot L \cdot C \cdot R_a + s \cdot L \cdot R_a + R_a} \]  

(5)

The angle \( \theta \) is obtained from the equation (5) shown below

\[ \theta = \tan^{-1}\left(\frac{\omega^2 \cdot L \cdot C \cdot R_a + \omega^2 \cdot L}{R_a}\right) \]  

(6)

The transfer function [2] for the above equivalent circuit with resistive and inductive load is obtained as

\[ \frac{V_o(s)}{V_i(s)} = \frac{D \cdot (R_a + sL)}{s^2 \cdot L \cdot C \cdot (R_a + sL) + s \cdot L + R_a + sL} \]  

(7)

The angle \( \theta \) is obtained from the equation (8) shown below

\[ \theta = \tan^{-1}\left(\frac{\omega L_a}{R_a}\right) - \tan^{-1}\left(\frac{\omega^3 L \cdot C \cdot L_a + \omega^2 L \cdot C \cdot R_a + \omega (L + L_a)}{R_a}\right) \]  

(8)

### III. PARTICLE SWARM BACTERIA FORAGING OPTIMIZATION TECHNIQUE

The conventional algorithm like dynamic programming, ziegler-nichols method and genetic algorithm solves the non-linear mathematical problems. The dynamic programming method has tedious steps and time consuming. The output converges at longer time. In ziegler-nichols is the basic method of obtaining the proportional integral and derivative gains of a particular control system. This method has larger settling time and peak overshoot, hence unsuitable for accurate control system problems. This paved the way for optimization of solving non-linear equations. The first and foremost is the concept of genetic algorithm. From this many new concepts have arrived like
swarm intelligence, ant colony and many other new concepts for solving the non-linear problems [7].

The particle swarm optimization is the behavior of colony of swarm of ants and flock of birds. The main objective of this optimization is finding the food in least possible time and path taken to reach the final point. Each particle is assumed to have position and velocity. In the search space the swarm of birds will move in random direction. When it finds the best position it will remember and communicate to other birds for the food. The position and velocities are adjusted depending on the food locations.

\[ V_{i}^{m+1} = w_{i} * V_{i}^{m} + C_{1} * rand1(LP_{i} - CP_{i}^{m}) + C_{2} * rand2(GP_{i} - CP_{i}^{m}) \]  

(9)

Where \( V \) is the velocity of particle, \( C_{1}, C_{2} \) are constants, \( LP_{i} \) is local position, \( CP_{i} \) is current position, \( GP_{i} \) is global position and \( w_{i} \) acceleration factor. If the position of all the particles are obtained then it is converged. Otherwise the second iteration will start by starting from local best position and global best position.

The bacteria foraging optimization basically evolves the concept of E.coli bacterium. The concept of E.coli is it searches for food and the bacteria grows, while when it is near with noxious substances it will retard [14]. There are three basic operations in bacterial foraging optimization method they are chemotaxis, reproduction and elimination and dispersal. The chemotactic step play important role for improving or retarding the strength of bacteria. When the environment is good and all the nutrients will be adding for bacteria and the size will improve. When the environment is not suitable for growth then it will retard and disappear.

The swimming operation allows in moving with other bacteria possibly in clockwise direction. The tumbling operation will be used when there is any change in operation. Normally it rotates in anti-clockwise direction especially when the noxious environment occurs. The second step of reproduction operation the bacteria with good nutrients will increase in size and asexually split into two parts.

The chemotactic step is given below.

\[ M(i, j, k, l) = M(i, j, k, l) + R(i) \frac{\Delta(i)}{\sqrt{\Delta(i)}} \]  

(10)

Where R(i) is step length, O is number of variables used in optimization and \( d_{\text{attractant}}, W_{\text{attractant}}, h_{\text{repellant}}, W_{\text{repellant}} \) are the different coefficients in the optimization algorithm. F(M,O(j, k, l)) is the objective function need to be minimized, \( \Delta(i) \) are the different coefficients are chosen properly for the optimized value of the passive filter design.

\[ F(M, O(j, k, l)) = \sum_{i=1}^{S} F(M, M^{i}(j, k, l)) \]

\[ = \sum_{i=1}^{S} - d_{\text{attractant}} \exp(-W_{\text{attractant}}) \sum_{m=1}^{p} (M_{m} - M^{i}_{m})^{2} + \sum_{i=1}^{S} h_{\text{repellant}} \exp(-W_{\text{repellant}}) \sum_{m=1}^{p} (M_{m} - M^{i}_{m})^{2} \]  

(11)

In the final step is the elimination and dispersal in this the good healthy bacteria will survive for the next iteration and unhealthy bacteria will be eliminated. The good food source location will be remembered and for the next iteration this process will continue till the convergence result is obtained. In this paper the objective function is given as

Objective function \( F = \frac{1}{\text{power factor}} \)

Subjected to

\( F_{\text{Min}} \leq F \leq F_{\text{Max}} \)

Where F (L, C) and the corresponding range of values indicate the minimum and maximum value of passive filter components.

4. Results and Discussion

4.1 Resistive Load

The variation of duty cycle with fundamental AC voltage is tabulated for resistive load of multiple pulse width modulated AC voltage controller. The tabulated values are shown from without filter. The load resistance used is 50 ohms.

Table I: PWM AC Voltage controller Resistive load without filter

<table>
<thead>
<tr>
<th>S.No</th>
<th>Duty Cycle</th>
<th>Fundamental output voltage in volts</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>0.1</td>
<td>5.68</td>
</tr>
<tr>
<td>2</td>
<td>0.2</td>
<td>34.27</td>
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<tr>
<td>3</td>
<td>0.3</td>
<td>48.76</td>
</tr>
<tr>
<td>4</td>
<td>0.4</td>
<td>64.81</td>
</tr>
<tr>
<td>5</td>
<td>0.5</td>
<td>84.77</td>
</tr>
<tr>
<td>6</td>
<td>0.6</td>
<td>93.01</td>
</tr>
<tr>
<td>7</td>
<td>0.7</td>
<td>128.2</td>
</tr>
<tr>
<td>8</td>
<td>0.8</td>
<td>140.4</td>
</tr>
<tr>
<td>9</td>
<td>0.9</td>
<td>167.6</td>
</tr>
</tbody>
</table>
### Table II: PWM AC Voltage controller Resistive load with filter

<table>
<thead>
<tr>
<th>S.No</th>
<th>Duty Cycle</th>
<th>Fundamental output voltage in volts</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>0.1</td>
<td>27.7</td>
</tr>
<tr>
<td>2</td>
<td>0.2</td>
<td>57</td>
</tr>
<tr>
<td>3</td>
<td>0.3</td>
<td>86.7</td>
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<tr>
<td>4</td>
<td>0.4</td>
<td>116.3</td>
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<tr>
<td>5</td>
<td>0.5</td>
<td>150.9</td>
</tr>
<tr>
<td>6</td>
<td>0.6</td>
<td>181.0</td>
</tr>
<tr>
<td>7</td>
<td>0.7</td>
<td>212.0</td>
</tr>
<tr>
<td>8</td>
<td>0.8</td>
<td>242.0</td>
</tr>
<tr>
<td>9</td>
<td>0.9</td>
<td>250.0</td>
</tr>
</tbody>
</table>

### Fig 4: Output Voltage waveform for resistive load without filter

### Fig 5: Total Harmonic distortion for resistive load without filter

### 4.2 Resistive and Inductive Load

The variation of duty cycle with fundamental AC voltage is tabulated for resistive-inductive load of multiple pulse width modulated AC voltage controller. The tabulated values are shown from without filter. The load resistance used is 50 ohms and load inductance used is 10 milli Henry.

#### Table III: PWM AC Voltage controller Resistive-Inductive load without filter

<table>
<thead>
<tr>
<th>S.No</th>
<th>Duty Cycle</th>
<th>Fundamental output voltage</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>0.1</td>
<td>7.92</td>
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<tr>
<td>2</td>
<td>0.2</td>
<td>19.26</td>
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<td>3</td>
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<td>91.76</td>
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<td>6</td>
<td>0.6</td>
<td>110.5</td>
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<tr>
<td>7</td>
<td>0.7</td>
<td>129.3</td>
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<tr>
<td>8</td>
<td>0.8</td>
<td>148.0</td>
</tr>
<tr>
<td>9</td>
<td>0.9</td>
<td>166.6</td>
</tr>
</tbody>
</table>

#### Table IV: PWM AC Voltage controller Resistive-Inductive load with filter

<table>
<thead>
<tr>
<th>S.No</th>
<th>Duty Cycle</th>
<th>Fundamental output voltage in volts</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>0.1</td>
<td>29</td>
</tr>
<tr>
<td>2</td>
<td>0.2</td>
<td>72</td>
</tr>
<tr>
<td>3</td>
<td>0.3</td>
<td>110</td>
</tr>
<tr>
<td>4</td>
<td>0.4</td>
<td>147</td>
</tr>
<tr>
<td>5</td>
<td>0.5</td>
<td>185.6</td>
</tr>
<tr>
<td>6</td>
<td>0.6</td>
<td>223</td>
</tr>
<tr>
<td>7</td>
<td>0.7</td>
<td>261</td>
</tr>
<tr>
<td>8</td>
<td>0.8</td>
<td>298</td>
</tr>
<tr>
<td>9</td>
<td>0.9</td>
<td>335</td>
</tr>
</tbody>
</table>

### Fig 6: Output Voltage waveform for resistive load with filter
A computer code has been done using Matlab m file programming. The design data are fed in to the program along with the subject to constraints is mentioned. The objective function is calculated for different iterations and it should have minimum value. The corresponding values of passive filter components are obtained from the program. The estimated values are simulated using MATLAB simulink environment to get the necessary sinusoidal output voltage and permissible limits of total harmonic distortion.

It is understood that by the application of filter the output voltage waveform becomes pure sinusoidal. The total harmonic distortion is high value without filter and it is with in permissible limits of 5 percentage for the resistive load. The passive filter components are obtained from the optimization algorithm as $L = 20$ milli Henry and capacitor value is 200 microfarad. The passive filter is also applied for resistive and inductive load. The output voltage is not purely sinusoidal because of multiple pulse width modulation. The percentage of harmonics is of high value. By using the filters the value of total harmonic distortion is reduced. The values of filter components are $L = 50$ milli Henry ; and $C = 300$ microfarad.

5. CONCLUSION

The performance analysis of multiple pulse width modulation fed ac voltage controller feeding resistive and resistive-inductive load is obtained with and without filter. This is done using analytical methods combined with optimization algorithm and simulation using MATLAB simulink. Some concluding interpretations from the exploration are as follows. The output voltage has improved significantly by using passive filter for both loads at any duty ratio of the ac voltage controller. In resistive load the output voltage is more than input voltage at duty ratio of 0.9 is due to the effect of capacitance value. The total harmonic distortion for the resistive load without filter is 113% while by incorporating the filter in the circuit the total harmonic distortion is 4.99% within the permissible limit of International electrical and electronics engineering standard. The output voltage waveform of pulse width modulation is not proper sine wave. After filtering operations the waveform is considered to purely sinusoidal. For resistive inductive load the waveform is sinusoidal. The waveform of current wave form is shifted by some angle due to the fact that load is inductive nature. This can averted by closed control of current makes the waveform sinusoidal.

6. REFERENCES


About Authors:

N.Murali has 10 years of teaching experience. Now he is working as a Consultant. His current areas of research are Optimization techniques, power electronics and drives, electrical machines and power electronics application in power system. He has published 6 research papers in international journals and 6 papers in national conferences. He is a Life member of System society of India.

Prof .Dr. V.BALAJI has 14 years of teaching experience. Now he is working as a Associate Professor in the school of Electrical Engineering, Bahir Dar University Ethiopia. His current areas of research are model predictive control, process control, communication and Fuzzy and Neural Networks. He has received world greatest teacher award, best teacher award, excellence in education award and Dr APJ Abdul Kalam award for young scientist. He has Published 50 research papers and two books. www.vbalaji.in website was created by him and study materials were uploaded. He is serving as an external supervisor in various universities He is an active member of ISTE, IAENG, IAOE, IACSIT, FMIAEME, LMIAOE, LM IACSIT, SMIRED, MIIRJC. . He is also serving as an Chief Editor, Associate editor, editorial board member and reviewer in various journals.