## **ADAM STEPHENSON**

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#### **SUMMARY**

Registered patent attorney and professional engineer with experience in patent drafting and prosecution in the telecommunication, electronics, optics, mechanical, software, business method, chemical, pharmaceutical, and biotech technologies. Counsels clients in trademark, copyright, and patent strategies and solutions. Excellent organizer who continuously standardizes, documents, and improves processes and systems engaging peers and customers in improvement efforts. An experienced problem solver and communicator who relishes tackling ambiguous and difficult situations and creating optimized outcomes.

# **PROFESSIONAL EXPERIENCE**

**ADAM R. STEPHENSON, LTD.** – Tempe, Arizona **Jan 2010 – Present** A firm focusing on patent, trademark, and copyright prosecution and evaluation.

**President** – Tempe, Arizona

Assisting clients with acquiring and evaluating intellectual property, including patents, copyrights, and trademarks. Services include drafting utility, provisional, and design patent applications; preparation of responses to actions taken by the Patent and Trademark Office; international patent and trademark filings; patentability, freedom-to-operate, technology clearance, and examination searches. Advises clients during pre-litigation negotiations and settlements and Uniform Domain Name Dispute Resolution Policy (UDRP) actions.

PHOENIX SCHOOL OF LAW. – Phoenix, Arizona

Arizona's most recently fully accredited private law school.

Jan 2011 – May 2011

## Adjunct Professor of Law - Tempe, Arizona

Instructing students in the law and mechanics of drafting design and utility patent applications. Course work includes patent prosecution strategies and drafting of Office Action responses and legal analysis involved in opinion work including patentability opinions and patent infringement and invalidity opinions.

**BOOTH UDALL, PLLC** – Tempe, Arizona May 2006 – Jan 2010
An intellectual property boutique firm of 5-6 attorneys handling patent and trademark prosecution.

## Associate and Patent Agent - Tempe, Arizona

Assisted clients with developing patent, trademark, and copyright strategies to create value and secure their products, services, and brand identity. Supported the firm's clients through drafting numerous patent applications and analyzing disclosures to determine optimum claim scope and potential prior art concerns. Managed and maintained the firm's practice management software and IT infrastructure day-to-day.

• Created winning solutions to patent problems involving patent applications and portfolios resulting in a number of allowances and traverses of serious claim rejections.

- Counseled and assisted in developing strategies for a number of firm clients in trademark and
  copyright infringement issues resulting in successful settlement and resolution of issues without
  requiring formal litigation.
- Collaborated with firm partners to create strategies for midsize businesses that resulted in the creation and development of valuable patent portfolios.
- Drafted and prosecuted numerous patent applications for complex technologies at the Masters and PhD level within budget and delivered on schedule.
- Supported the growth of the firm from two to six attorneys in a paperless electronic office using practice management software and IT infrastructure resulting in optimal use of staff resources and maximizing attorney working flexibility.

## INTEL CORPORATION – Chandler, Arizona 2000-2006

The world's major manufacturer of microprocessors with over 75,000 employees globally.

# Factory Engineer – Chandler, Arizona

2003-2006

Created and qualified control programs (recipes) for equipment processing 300mm wafers through the die preparation portion of the chip assembly process. Handled error recovery and processing issues as part of a four-shift team of engineers sustaining Intel's Assembly Technology Development manufacturing floor.

- Applied advanced statistical experimental designs to characterize the behavior of a particular process tool resulting in a 90% reduction in excursions related to misprocessing by the tool.
- Created an ASP.Net website that automated use of models created using the experimental designs to enable generation of optimized recipe parameters resulting in 60% reduction in setup wafers needed to perform a new product introduction.

#### **Pathfinding Module Engineer** – Chandler, Arizona **2003**

Developed lithography processes for stepper, scanner, and aligner exposure tools along with corresponding metrology for advanced, experimental, far backend interconnect semiconductor and chip assembly processes.

- Proved the concept of an alternate process flow for an interconnect structure after the primary flow failed after 4-5 months of development resulting in formation of a formed interconnect in one week.
- Migrated multiple existing process layers from one developer solution to another resulting in the use of a more practical and environmentally friendly chemical and minimal impact to factory throughput.

# Lithography Process Engineer – Chandler, Arizona 2000-2003

- Developed, qualified, and implemented over 25 process and equipment changes for a fleet of 13-25 lithography tracks resulting in significant cost reduction and equipment/process improvement.
- Participated in development of two new lithography process layers for Intel's Liquid Crystal on Silicon (LCOS) process in a high volume manufacturing environment resulting in identifying a

resolution to a show stopping corrosion defect issue that matured into an issued U.S. patent and multiple foreign patents.

# **EDUCATION/PROFESSIONAL**

JD, cum laude – Phoenix School of Law, 2008, Charter Class Member
 MSE Chemical Engineering – Arizona State University, 2004
 BS Chemical Engineering – Brigham Young University, 1998

Supreme Court of Arizona, 2008
Federal District Court, District of Arizona, 2008
United States Patent and Trademark Office: Agent 2006, Attorney 2008
Registered Professional Engineer, Chemical Engineering – State of Arizona, 2005 - present

Member AIPLA

# **PUBLICATIONS**

Adam R. Stephenson, <u>A View of the Future in Semiconductor Process: Patent Prosecution in Class 438</u>
<u>Under the United States Patent and Trademark Office's Final Claims and Continuations Rules</u>, 8 Wake
Forest Intell. Prop. L. J. 272 (2008)

Adam R. Stephenson, <u>Arizona Juvenile Law Legal Research: Resources and Strategies</u>, 2 Phoenix L. Rev. 194 (2009)