Winter February 3, 2022

# Making Nöther Matrics 

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## Circuits Using Nöther Matrics [NM]

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An arrow, $a[i, j]$, in an m-polygon goes from vertex $i$, its source, to vertex $j$, its destination, $\mathrm{i} \leftrightarrow \mathrm{j}$. It is not two dimensional in a Nöther Algebra. It is a non-zero term in an sXs matrix whose terms are zero with this one exception, the term in row i , column j equals 1 . In my work arrows always belong to circuits. A circuit with $s$ arrows has a given relative order such that the destination of arrow $a[i, j]$, is the source of the next arrow, $a[j, k]$. The source of the arrow $a[j, k]$ is the destination of its predecessor, $a[i, j]$. Arrows in a circuit describe a round trip. Every arrow in an m-polygon belongs to at least one circuit. They never are alone. To show this the symbol for an arrow uses a special notation. Arrow a[i,j] in an s-circuit has the special symbol G[i,j,s].

A matric in N -algebra has s rows and columns to represent a one-way arrow in an s-circuit. Matric distinguishes it from matrix. An s-circuit is the sequence $\left\{\mathrm{a}\left[i_{1}, j_{1}\right], \mathrm{a}\left[i_{1}, j_{2}\right], \mathrm{a}\left[i_{2}, j_{3}\right], \ldots, \mathrm{a}\left[i_{s}, i_{1}\right]\right\}$.
As a matric it becomes the product
$\mathrm{G}\left[i_{1}, j_{1}, \mathrm{~s}\right] . \mathrm{G}\left[i_{1}, j_{2}, \mathrm{~s}\right] . \mathrm{G}\left[i_{2}, j_{3}, \mathrm{~s}\right], \ldots, \mathrm{G}\left[i_{s}, j_{s}, \mathrm{~s}\right]=\mathrm{G}\left[i_{1}, i_{1}, \mathrm{~s}\right]$, an sXs matric
with one term $=1$ at row $i_{1}$, column $i_{1}$, a diagonal. All its other terms are 0. Because the same s appears in each G[.], in most cases s is omitted since it is clear from the context. This algebra uses an sXs matrix to identify an arrow in the s-circuit.

## 1. Simple Circuits

$\operatorname{In}[0]:=\operatorname{Array}[0 \&,\{3,3\}]$
Out [0] $=\{\{0,0,0\},\{0,0,0\},\{0,0,0\}\}$
$\ln [\cdot]:=$ MatrixForm[Array[0 \& , \{3, 3\}]]
Out[-]//MatrixForm=

$$
\left(\begin{array}{lll}
0 & 0 & 0 \\
0 & 0 & 0 \\
0 & 0 & 0
\end{array}\right)
$$

$\ln [\cdot]:=G[1,2]:=\{\{0,1,0\},\{0,0,0\},\{0,0,0\}\}$
$\operatorname{In}[-]:=$ MatrixForm[G[1, 2] ]

## Out[0]/MatrixForm=

$$
\left(\begin{array}{lll}
0 & 1 & 0 \\
0 & 0 & 0 \\
0 & 0 & 0
\end{array}\right)
$$

$\ln [\cdot]:=G[2,3]:=\{\{0,0,0\},\{0,0,1\},\{0,0,0\}\}$
$\ln [\cdot]:=$ MatrixForm[G[2, 3] ]
Out[-]//MatrixForm=

$$
\left(\begin{array}{lll}
0 & 0 & 0 \\
0 & 0 & 1 \\
0 & 0 & 0
\end{array}\right)
$$

$\ln [\cdot]:=\mathrm{G}[3,1]:=\{\{0,0,0\},\{0,0,0\},\{1,0,0\}\}$
$\ln [\rho]:=$ MatrixForm[G[3, 1] ]

## Out[0]/MatrixForm=

$$
\left(\begin{array}{lll}
0 & 0 & 0 \\
0 & 0 & 0 \\
1 & 0 & 0
\end{array}\right)
$$

The symbol for the simple circuit $(1,2,3)$ is the product of 3 matrices, one matric for each one-way arrows in the simple 3-circuit. The term indicates a round trip starting from vertex 1 . It appears as the first term on the diagonal of the N -matric that shows the product of the 3 one-way arrows in their matrics.
$\ln \left[{ }^{\circ}\right]=$ MatrixForm[G[1, 2].G[2, 3].G[3, 1] ]
Out[•]//MatrixForm=

$$
\left(\begin{array}{lll}
1 & 0 & 0 \\
0 & 0 & 0 \\
0 & 0 & 0
\end{array}\right)
$$

$\ln [\cdot]:=G[1,1]:=\{\{1,0,0\},\{0,0,0\},\{0,0,0\}\}$
$\operatorname{In}[-]:=$ MatrixForm[G[1, 1] ]
Out[ - ]//MatrixForm=
$\left(\begin{array}{lll}1 & 0 & 0 \\ 0 & 0 & 0 \\ 0 & 0 & 0\end{array}\right)$
$\ln [\cdot]:=\mathrm{G}[1,2] \cdot \mathrm{G}[2,3] \cdot \mathrm{G}[3,1]$
Out[0]= $\{\{1,0,0\},\{0,0,0\},\{0,0,0\}\}$

As claimed $\mathrm{G}[1,1]=\mathrm{G}[1,2] \cdot \mathrm{G}[2,3] \cdot \mathrm{G}[3,1]$
For the 3 -circuit starting with vertex $3,(3,1,2)$, the matric has its one non zero term in row 3 , column 3 . The reader can show matric for this product is G[3,3].
$\ln [\cdot]:=$ MatrixForm[G[3, 1].G[1, 2].G[2, 3]]
Out[ $\bullet$ ]//MatrixForm=

$$
\left(\begin{array}{lll}
0 & 0 & 0 \\
0 & 0 & 0 \\
0 & 0 & 1
\end{array}\right)
$$

For the 3 -circuit starting with vertex 2 , ( $2,3,1$,), the matric has one non zero term in row 2, column 2.
$\ln [\cdot]=$ MatrixForm[G[2, 3].G[3, 1].G[1, 2]]
Out[0]//MatrixForm=

$$
\left(\begin{array}{lll}
0 & 0 & 0 \\
0 & 1 & 0 \\
0 & 0 & 0
\end{array}\right)
$$

These three NA matrices for the same simple 3 - circuit differ. Each has its own matric. An incorrect order of these matrics gives 0 as the next example shows.
$\operatorname{In}[\cdot]:=\operatorname{MatrixForm}[\mathrm{G}[2,3] \cdot \mathrm{G}[1,2] \cdot \mathrm{G}[3,1]]$
Out[0]//MatrixForm=

$$
\left(\begin{array}{lll}
0 & 0 & 0 \\
0 & 0 & 0 \\
0 & 0 & 0
\end{array}\right)
$$

Let $v[i, j]$ denote the value of the one-way arrow $a[i, j]$. The term $v[1,2]$ denotes the value of the arrow $\mathrm{a}[1,2]$. The matric showing the value of the round trip starting from vertex 1 and returning to vertex 1 is the following matric $\mathrm{V}[1]$. It has only one non term $(1,1)$ on the diagonal.

$$
\mathrm{V}[1]:=\left(\begin{array}{ccc}
v[1,2] * v[2,3] * v[3,1] & 0 & 0 \\
0 & 0 & 0 \\
0 & 0 & 0
\end{array}\right)
$$

The second matric is $\mathrm{V}[2]$.
$\mathrm{V}[2]:=\left(\begin{array}{ccc}0 & 0 & 0 \\ 0 & v[2,3] * v[3,1] * v[1,2] & 0 \\ 0 & 0 & 0\end{array}\right)$
The third matric is V [3]

$$
\mathrm{v}[3]:=\left(\begin{array}{ccc}
0 & 0 & 0 \\
0 & 0 & 0 \\
0 & 0 & v[3,1] * v[1,2] * v[2,3]
\end{array}\right)
$$

Each $\mathrm{V}[\mathrm{i}]$ is a different matric for the value of the different round trips of the simple 3-circuit. Although the round trips start from different vertexes, they have equal value shown by their matrics.

## 2. Super Circuits

Warning: Mathematica arranges the terms in the product in lexicographical order. This is incorrect. The sequence of arrows in the circuit is the correct order.

A super circuit is a set of simple circuits whose arrows do not intersect and share at least one vertex. The next two simple circuits

$$
\{(5,1),(1,4),(4,2),(2,5)\} \text { and }\{(5,3),(3,4),(4,5)\}
$$

in the pentagon share no arrows and share vertex 5 . They make the following super circuit. Its $5 \times 5$ matric is $\mathrm{G}[5,5$ ]

$$
\{(5,1),(1,4),(4,2),(2,5),(5,3),(3,4),(4,5)\} .
$$

The procedure step by step follows. To save space I replace G by g in the matric.
$\ln [\bullet]:=$
$\ln [\bullet]:=$
$\ln [\bullet]:=$
$\ln [\bullet]:=$
$\ln [\bullet]:=$

$$
\begin{aligned}
& \text { g14 }:=\{\{0,0,0, a[1,4], 0\},\{0,0,0,0,0\}, \\
& \{0,0,0,0,0\},\{0,0,0,0,0\},\{0,0,0,0,0\}\}
\end{aligned}
$$

$$
\begin{aligned}
& g 53:=\{\{0,0,0,0,0\},\{0,0,0,0,0\},\{0,0,0,0,0\} \\
& \quad\{0,0,0,0,0\},\{0,0, a[5,3], 0,0\}\}
\end{aligned}
$$

$$
\operatorname{g25}:=\{\{0,0,0,0,0\},\{0,0,0,0, a[2,5]\}
$$

$$
\{0,0,0,0,0\},\{0,0,0,0,0\},\{0,0,0,0,0\}\}
$$

$\mathrm{g} 31:=\{\{0,0,0,0,0\},\{0,0,0,0,0\}$,
$\{a[3,1], 0,0,0,0\},\{0,0,0,0,0\}$,
$\{0,0,0,0,0\}\}$
g12 $:=\{\{a[1,2], 0,0,0,0\},\{0,0,0,0,0\}$,

$$
\{0,0,0,0,0\},\{0,0,0,0,0\},\{0,0,0,0,0\}\}
$$

$\ln [\bullet]:=$

$$
\begin{aligned}
& \text { g23 }:=\{\{0,0,0,0,0\},\{0,0, a[2,3], 0,0\}, \\
& \quad\{0,0,0,0,0\},\{0,0,0,0,0\},\{0,0,0,0,0\}\}
\end{aligned}
$$

$$
\begin{aligned}
& \text { g34 }:=\{\{0,0,0,0,0\},\{0,0,0,0,0\} \\
& \quad\{0,0,0, a[3,4], 0\},\{0,0,0,0,0\} \\
& \{0,0,0,0,0\}\}
\end{aligned}
$$

$$
\text { g45 : = \{\{0, 0, 0, 0, 0\}, }\{0,0,0,0,0\},\{0,0,0,0,0\},
$$

$$
\{0,0,0,0, a[4,5]\},\{0,0,0,0,0\}\}
$$

$$
\begin{aligned}
& \text { g51 }:=\{\{a[5,1], 0,0,0,0\},\{0,0,0,0,0\}, \\
& \quad\{0,0,0,0,0\},\{0,0,0,0,0\},\{0,0,0,0,0\}\}
\end{aligned}
$$

g42 : = \{\{0, 0, 0, 0, 0\}, \{0, 0, 0, 0, 0\}, \{0, 0, 0, 0, 0\}, $\{0, a[4,2], 0,0,0\},\{0,0,0,0,0\}\}$

The next 3 steps make the following super circuit. (g51.(g14.g42)).g25) (g53.g34).g45) = ((g51.(g14.g42)).g25).((g53.g34).g45)
Note the positions of the parentheses.
$\ln [0]:=((g 51 \cdot(g 14 \cdot g 42)) \cdot g 25)$
$\{\{0,0,0,0, a[1,4] * a[2,5] * a[4,2] * a[5,1]\}$,
$\{0,0,0,0,0\},\{0,0,0,0,0\},\{0,0,0,0,0\}$,
$\{0,0,0,0,0\}\}$
$\ln [0]=(\mathrm{g} 53 . \mathrm{g} 34) . \mathrm{g} 45$
$\{\{0,0,0,0,0\},\{0,0,0,0,0\},\{0,0,0,0,0\}$,

$$
\{0,0,0,0,0\},\{0,0,0,0, a[3,4] * a[4,5] * a[5,3]\}\}
$$

$\ln [0]=((\mathrm{g} 51 \cdot(\mathrm{~g} 14 \cdot \mathrm{~g} 42)) \cdot \mathrm{g} 25) \cdot((\mathrm{g} 53 \cdot \mathrm{~g} 34) \cdot \mathrm{g} 45)$

$$
\begin{gathered}
\{\{0,0,0,0, a[1,4] * a[2,5] * a[3,4] * a[4,2] * \\
\quad a[4,5] * a[5,1] * a[5,3]\},\{0,0,0,0,0\}, \\
\{0,0,0,0,0\},\{0,0,0,0,0\},\{0,0,0,0,0\}\}
\end{gathered}
$$

This gives the super circuit in the first term on the diagonal of the matric product ((g51.(g14.g42)).g25).((g53.g34).g45). Mathematica shows the factors in lexicographical order, not the correct super circuit order. Next is the procedure for $\mathrm{g} 23 . \mathrm{g} 31=\mathrm{g} 21$
$\ln [\cdot]:=$ g23.g31

$$
\begin{aligned}
& \{\{0,0,0,0,0\},\{a[2,3] * a[3,1], 0,0,0,0\} \\
& \{0,0,0,0,0\},\{0,0,0,0,0\},\{0,0,0,0,0\}\}
\end{aligned}
$$

$$
g 23 . g 31=g 21
$$

$\ln [\mathrm{g}:=(\mathrm{g} 23 . \mathrm{g} 31) \cdot \mathrm{g} 12$
$\{\{0,0,0,0,0\},\{a[1,2] * a[2,3] * a[3,1], 0,0,0,0\}$, $\{0,0,0,0,0\},\{0,0,0,0,0\},\{0,0,0,0,0\}\}$
$(\mathrm{g} 23 . \mathrm{g} 31) \cdot \mathrm{g} 12=\mathrm{g} 21 . \mathrm{g} 12=\mathrm{g} 22$
$\operatorname{In}[0]:=(\mathrm{g} 51 \cdot \mathrm{~g} 14) \cdot \mathrm{g} 45$

$$
\begin{aligned}
& \{\{0,0,0,0, a[1,4] * a[4,5] * a[5,1]\},\{0,0,0,0,0\}, \\
& \{0,0,0,0,0\},\{0,0,0,0,0\},\{0,0,0,0,0\}\}
\end{aligned}
$$

The final step is (g51.g14).g45 = g54.g45. It is the fifth term of the diagonal matric $\mathrm{g}[5,5]$.
$\ln [0]:=((\mathrm{g} 51 \cdot \mathrm{~g} 14) \cdot \mathrm{g} 45)$

$$
\begin{aligned}
& \{\{0,0,0,0, a[1,4] * a[4,5] * a[5,1]\},\{0,0,0,0,0\}, \\
& \{0,0,0,0,0\},\{0,0,0,0,0\},\{0,0,0,0,0\}\} \\
& ((\mathrm{g} 51 . g 14) \cdot \mathrm{g} 45) \cdot((\mathrm{g} 23 . \mathrm{g} 31) \cdot \mathrm{g} 12)
\end{aligned}
$$

```
\(\{\{0,0,0,0,0\}\),
    \(\{0,0,0,0, a[1,2] * a[1,4] * a[2,3] * a[3,1] *\)
        \(a[4,5] * a[5,1]\},\{0,0,0,0,0\},\{0,0,0,0,0\}\),
    \(\{0,0,0,0,0\}\}\)
```


## 3. Valuation of a Circuit

The results in the preceding two sections show that the value of a circuit is given by its matric whose terms are arrow values.

Let $1+\rho_{i, j}$ denote the value of arrow $a[i, j]$. The value of the 3 - circuit, $\mathrm{V}[\theta]$, is

$$
\begin{aligned}
V[\theta] & =\left(1+\rho_{12}\right) *\left(1+\rho_{23}\right) *\left(1+\rho_{31}\right)= \\
& =1+\rho_{12}+\rho_{23}+\rho_{31}+\rho_{12} \rho_{23}+\rho_{12} \rho_{31}+\rho_{23} \rho_{31}+\rho_{12} \rho_{23} \rho_{31}
\end{aligned}
$$

The arithmetic mean of the value of the arrows in a circuit exceeds the geometric mean of their values unless $\rho_{\mathrm{ij}}=\rho$. Given the mean, the geometric mean varies inversely with the dispersion of its terms. The closer together are $\rho_{i, j}$, the closer the geometric mean to the arithmetic mean. Given the arithmetic mean, the more nearly equal are $\left(1+\rho_{i, j}\right)$ in the circuit, the bigger the circuit value.

For $\rho_{l} \geq 0$,
(1) $\Sigma_{1}^{n}\left(1+\rho_{i}\right)<\Pi_{1}^{n}\left(1+\rho_{i}\right)$.

The subscript identifies the owner of the arrow. Let $w_{i}$ denote the return to owner $\mathrm{i}, \mathrm{i}=1,2, \ldots, \mathrm{n}, \mathrm{n}=\mathrm{m}(\mathrm{m}-1) / 2$.
Let $\mathrm{V}(\theta)$ in (2) denote the value of circuit $\theta$.
(2) $\mathrm{V}(\theta)=\Pi_{1}^{n}\left(1+\rho_{i}\right)$.

The core constraint is inequality (3)
(3) $\sum_{i \in \theta} w_{i} \geq \mathrm{V}(\theta)$

If $w_{i}=1+\rho_{i}$, then the smallest acceptable payoff to owner i cannot satisfy (3) owing to inequality (1). The core constraints are not feasible. Changing to logs gives feasibility. Let the owner of vertex i get $\log w_{i}$ instead of $w_{i}$. The $\log$ of the value replaces value. Instead of (3), there is
(4) $\sum_{i \in \theta} \log w_{i} \geq \log V(\gamma)=\log \Pi_{i \in \theta}\left(1+\rho_{i}\right)=\sum_{i \in \theta} \log \left(1+\rho_{i}\right)$.

Log $w_{i}$ can satisfy (4) with equality even if some $\rho_{i}=0$. Such arrows are unproductive. Their value $=1$ and $\log 1=0$.

## 4 Undirected Edges

The undirected edge from vertex $i$ to vertex $j$ is denoted by $i \leftharpoondown j$. It differs from the pair of oppositely oriented arrows $i \rightarrow j$ and $j \mapsto i$. The Nöther matric for the undirected edge connecting vertex $i$ and vertex $j$ is
$\ln [\cdot]:=\mathbf{a}:=\{\{0,1\},\{\mathbf{1}, 0\}\}$
In[•]:= MatrixForm[a]
Out[0]/MatrixForm=
$\left(\begin{array}{ll}0 & 1 \\ 1 & 0\end{array}\right)$

The matric for the arrow $1 \rightarrow 2$ is $a$. The matric for the arrow $2 \mapsto 1$ is $b$
$\ln [\varnothing]:=\mathrm{b}:=\{\{\mathbf{1}, 0\},\{0,1\}\}$
In[0]:= MatrixForm[b]
Out[0]/MatrixForm=
$\left(\begin{array}{ll}1 & 0 \\ 0 & 1\end{array}\right)$
$\ln [0]:=\mathbf{a} \cdot \mathbf{b}$
Out[ 0$]=\{\{0,1\},\{1,0\}\}$

Thus a.b $=a$ and $\{1 \mapsto 2,2 \mapsto 1\}=1 \mapsto 2$ as required.
$\ln [\cdot]:=\operatorname{Det}[\mathrm{a} \cdot \mathrm{a}]$
Out[0]= 1
$\ln [-]:=$ Transpose[a]
Out[ 0$]=\{\{0,1\},\{1,0\}\}$

The transpose of $a$ is $b$.
$\ln [\rho]:=\mathrm{tl}:=\{1 \rightarrow 2,2 \rightarrow 3,3 \mapsto 4,4 \mapsto 2,2 \mapsto 1\}$
$\ln [\cdot]:=\operatorname{Array}[0 \&,\{4,4\}]$
Out [0]= $\{\{0,0,0,0\},\{0,0,0,0\},\{0,0,0,0\},\{0,0,0,0\}\}$
$\ln [\cdot]:=g 12:=\{\{0,1,0,0\},\{0,0,0,0\},\{0,0,0,0\}$, $\{0,0,0,0\}\}$
$\ln [\cdot]:=g 23:=\{\{0,0,0,0\},\{0,0,1,0\},\{0,0,0,0\}$, $\{0,0,0,0\}\}$
$\ln [\cdot]:=\mathrm{g} 34:=\{\{0,0,0,0\},\{0,0,0,0\},\{0,0,0,1\}$, $\{0,0,0,0\}\}$
$\ln [\rho]:=g 42:=\{\{0,0,0,0\},\{0,0,0,0\},\{0,0,0,0\}$, $\{0,1,0,0\}\}$
$\ln [\rho]:=\mathrm{g} 21:=\{\{0,0,0,0\},\{1,0,0,0\},\{0,0,0,0\}$, $\{0,0,0,0\}\}$
$\ln [\rho]=$ g12.g23
$\ln [\cdot]:=\operatorname{g13}:=\{\{0,0,1,0\},\{0,0,0,0\},\{0,0,0,0\}$, $\{0,0,0,0\}\}$
$\ln [\cdot]:=$ g13.g34
$\ln [\cdot]:=\operatorname{g14}:=\{\{0,0,0,1\},\{0,0,0,0\},\{0,0,0,0\}$, $\{0,0,0,0\}\}$
$\ln [\square]:=$ g14.g42
$\ln [\cdot]:=\operatorname{g12}:=\{\{0,1,0,0\},\{0,0,0,0\},\{0,0,0,0\}$, $\{0,0,0,0\}\}$
$\ln [0]:=$ g12.g21
$\ln [\cdot]:=\operatorname{g11}:=\{\{1,0,0,0\},\{0,0,0,0\},\{0,0,0,0\}$, $\{0,0,0,0\}\}$

The Nöther matric of tl is the product of the 5 matrics; g12.g23.g34 . g42 .g21 = g11, the circuit tl
tl $:=\{1 \mapsto 2,2 \mapsto 3,3 \mapsto 4,4 \mapsto 2,2 \mapsto 1\}$
The adjacency matrix for tl is atl
$\ln [0]:=\operatorname{atl}:=\{\{0,1,0,0\},\{1,0,1,0\},\{0,0,0,1\}$, $\{0,1,0,0\}\}$
$\operatorname{In}[\varnothing]:=$ gatl := makeGrph[atl, 3]
$\ln [\cdot]:=$ gatl

Out[•]=


The Undirected Edge $1 \multimap 2$ becomes the pair of oppositely Directed Edges $1 \rightarrow 2$ \& $2 \rightarrow 1$ in the graph.
$\ln []^{2}:=$ EulerianGraphQ[gatl]
Outfol= True
$\operatorname{In}[-]:=$ HamiltonianGraphQ[gatl]
Out[0]= False

Therefore, an Eulerian graph can have two oppositely oriented arrows (Undirected Edge) made by two Directed Edges. This is impossible for a Hamiltonian graph.

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Program
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